Bifrost Arithmetic Functional Units

- **Executes quad-parallel scalar operations**
  - 4x32-bit multiplier FMA
  - 4x32-bit adder ADD
  - Adder includes special function unit

- **Smaller and more area efficient**

- **Simplified layout eases compilation**
  - Better scheduling in today's code
  - Better utilization

- **One instruction word contains two instructions**

EMBARGOED UNTIL 11pm EDT on Sunday, May 29

ARM