

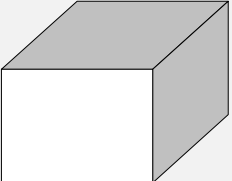
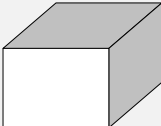
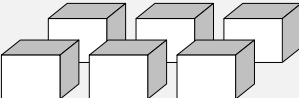
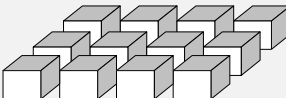




Future Transition			
Gen <i>n</i> Process	Gen <i>n</i> +1 Process		
	<2 X Cost		Processed Wafer Cost
	1/2 Size		Transistor Size
	2X		Number of Transistors
	<1X Cost		Cost/Transistor