

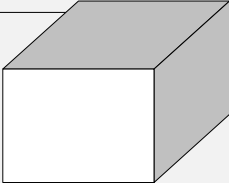
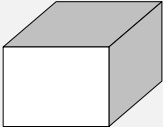
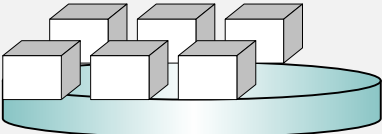
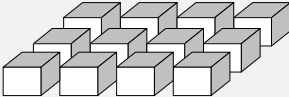




Past Transition

Past Transition		Gen n Process	Gen $n+1$ Process		
	1X Cost		Processed Wafer Cost	1X	
	1/2 Size		Transistor Size	1/2	
	2X		Number of Transistors	2X	
	1/2 Cost		Cost/Transistor	1/2	