

IvyTown Die Layout



C:\USER\DATA2\Intel\Intel公式資料\写真\Nehalem系\Westmere-EX

14 Ivy Bridge CPU cores
 37.5 MB LL Cache
 22nm Bulk FinFET Process
 541 mm²
 4.31B Transistors

4-Channel DRAM IF
 (8 DDR3 Channels)
 60 lanes QPI
 (3 links)
 40 lanes PCI Express
 4 lanes DMI