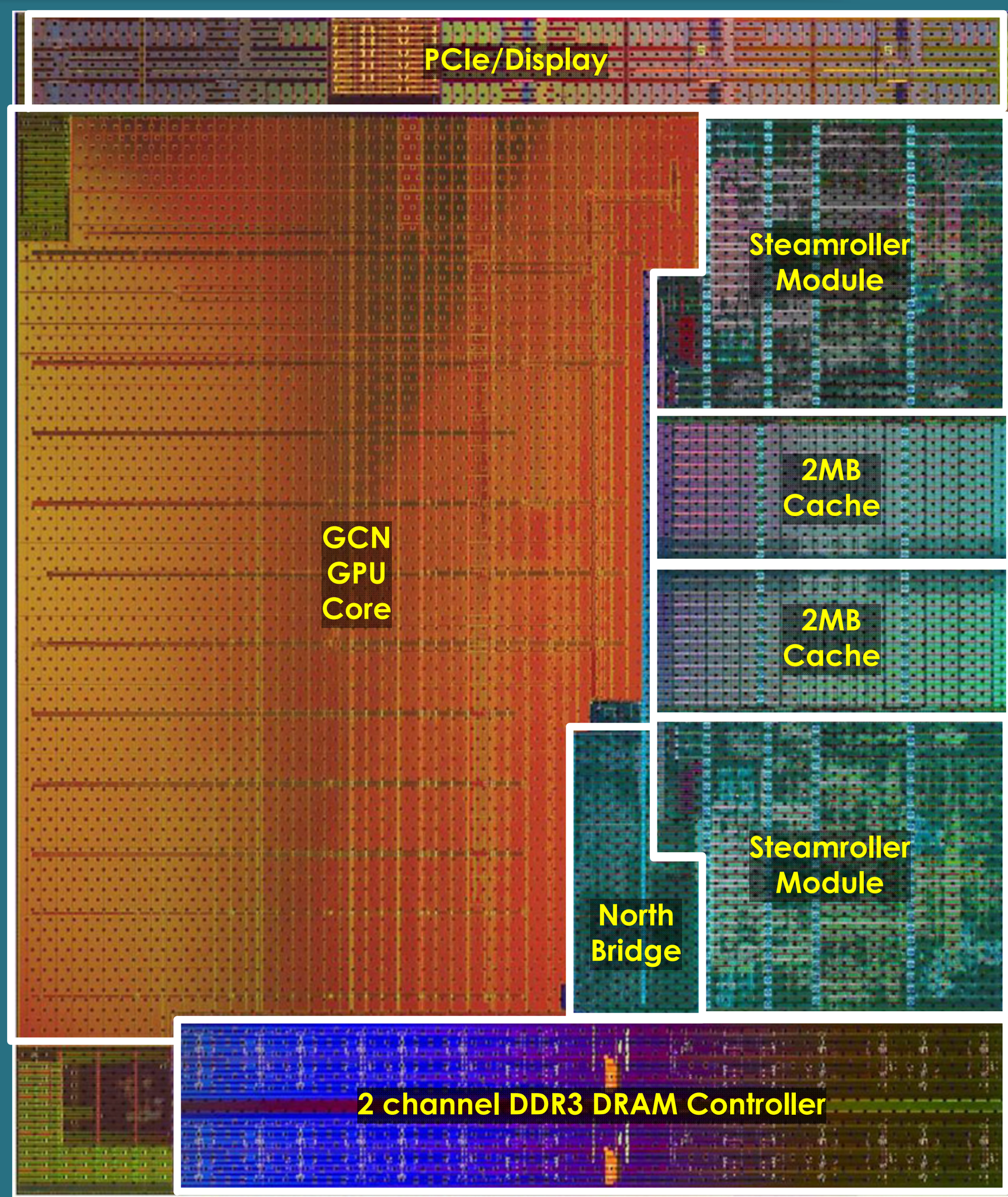
AMD Kaveri, Trinity and Llano Die

Kaveri Die Layout



2 CPU Modules	28 nm SHP Bulk Process
4 CPU cores	(GLOBALFOUNDRIES)
GPU core (8 CU, 512 MADs)	2.41B transistors
2x 2MB L2 Cache	245 mm2
PCI Express Gen3 16 Lanes	
PCI Express Gen2 8 Lanes	

Copyright © 2014 Hiroshige Goto All rights reserved.