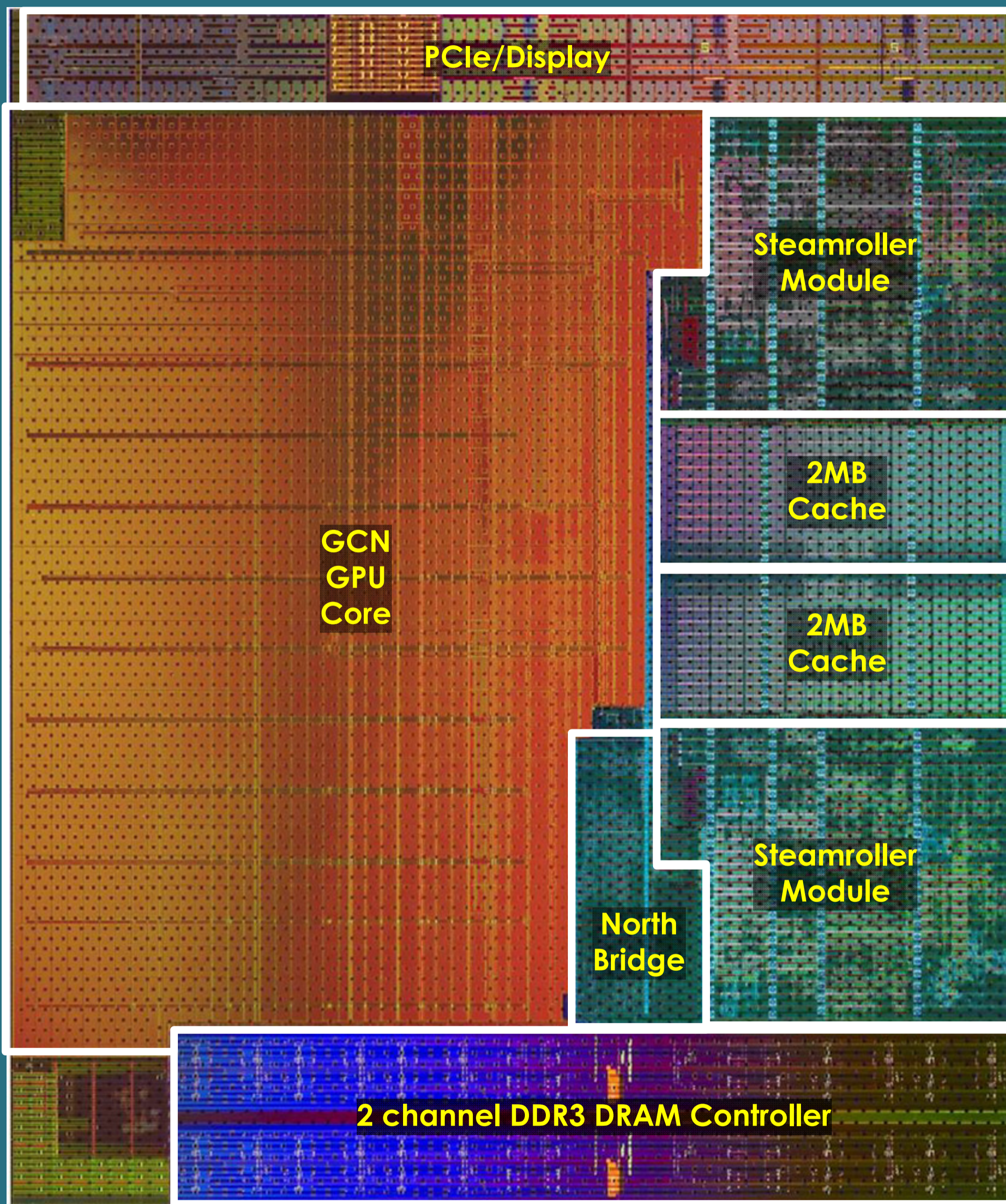


AMD Kaveri, Trinity and Llano Die

Kaveri Die Layout



2 CPU Modules
4 CPU cores
GPU core (8 CU, 512 MADs)
2x 2MB L2 Cache
PCI Express Gen3 16 Lanes
PCI Express Gen2 8 Lanes

28 nm SHP Bulk Process
(GLOBALFOUNDRIES)
2.41B transistors
245 mm²