

ARM Cortex-A12 Pipeline

				1
			Fetch 0-3	2
				3
				4
				5
			Predecode	6
			Decode	7
			Rename	8
Decode	Dispatch	Dispatch	Dispatch	9
Rename	Address	Multiply	Integer	10
Dispatch	Data Cache	Multiply	Integer	11
Ex		WriteBack	WriteBack	WriteBack
Ex	WriteBack	Multiply	Integer	13
Ex	Load/Store			14
Ex				15
Ex				16
Ex				17
Ex				18
Ex				19
Ex				20
Ex				21
WriteBack				22
VFP/NEON				23
				24

ARM Cortex-A9 Pipeline

			1
		Fetch	2
			3
			4
		Decode	5
		Rename	6
		Dispatch	7
		Integer	8
		WriteBack	9
			10
			11
			12
			13
			14
			15
			16
			17
			18
			19
			20
			21
			22
			23
			24