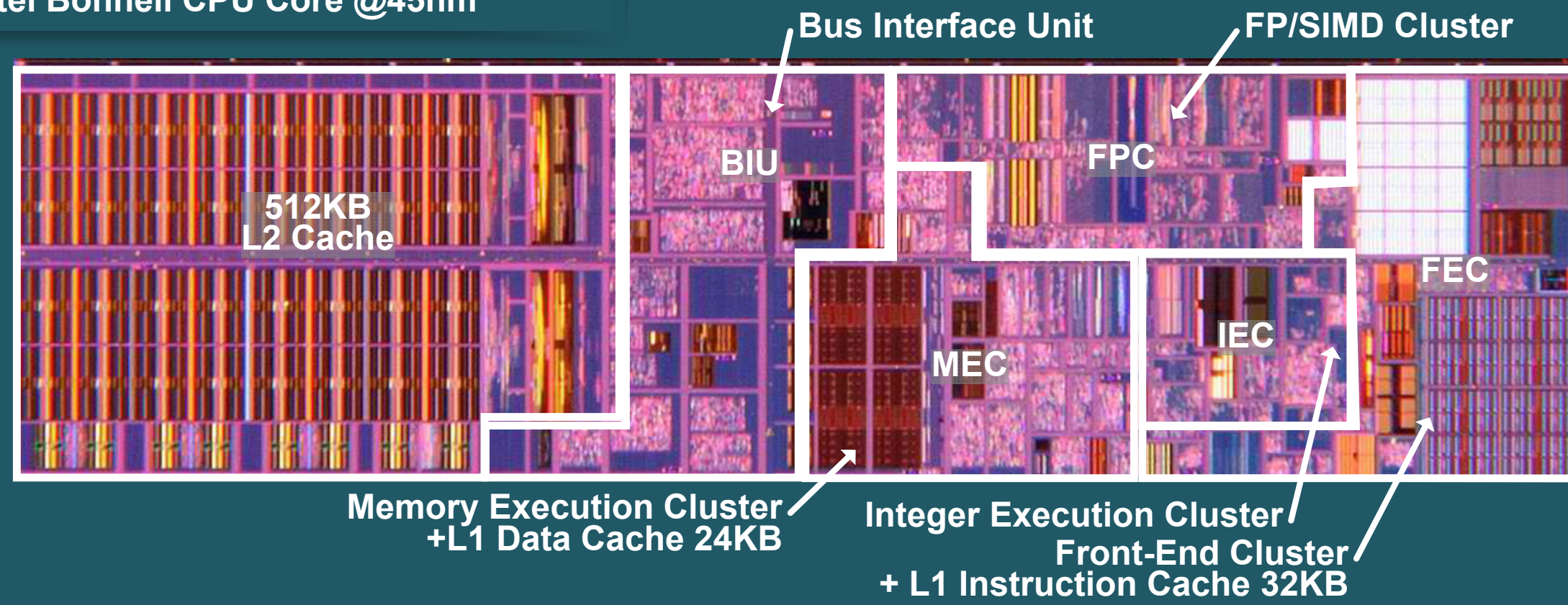


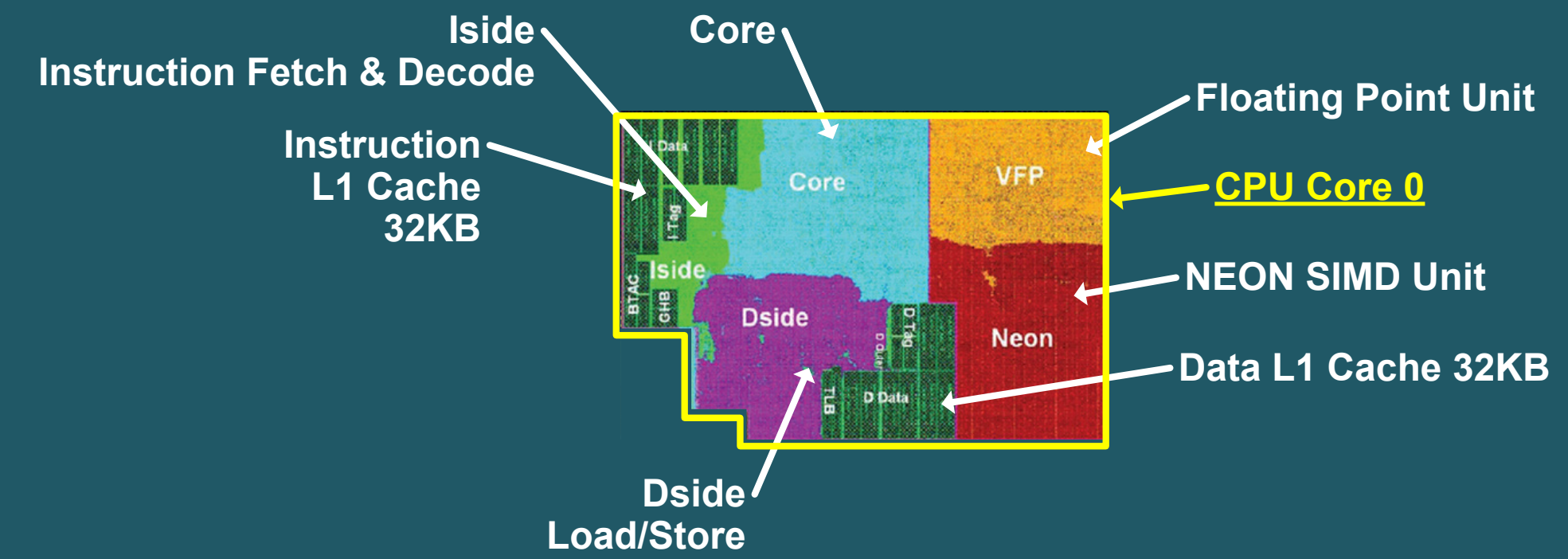
Intel LPIA Cores vs ARM Cores

Intel Bonnell CPU Core @45nm



45 nm LP bulk Process (Intel)
 2-Decode In-Order
 24KB L1 Data Cache
 32KB L1 Instruction Cache
 512KB L2 Cache
 14mm² CPU Core+L2
 <9mm² CPU Core
 (w/o bus unit 6.x mm²)

ARM Cortex-A9 @40nm



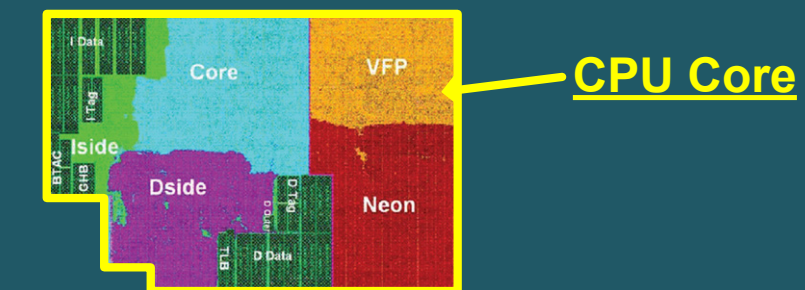
40 nm bulk G Process (TSMC)
 2-Decode Out-of-Order
 32KB L1 Data Cache
 32KB L1 Instruction Cache
 Up to 2MB L2 Cache
 1.35-2.6 mm²
 (1 CPU Core)

Intel Saltwell CPU Core @32nm



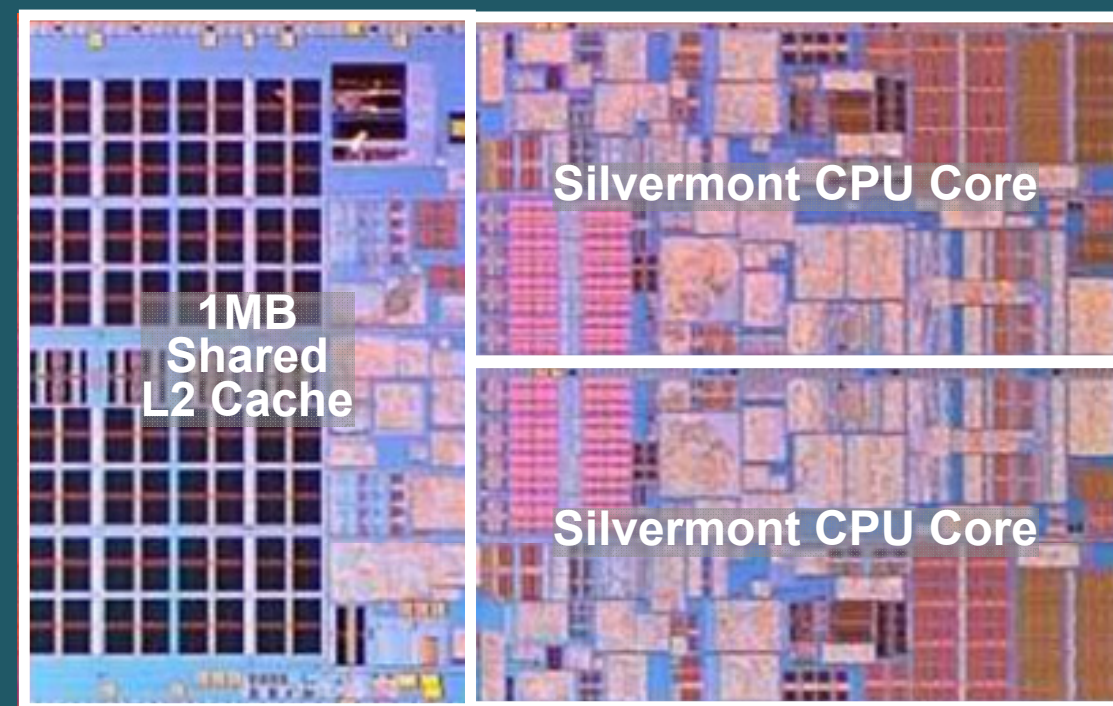
32 nm LP bulk Process (Intel)
 2-Decode In-Order
 24KB L1 Data Cache
 32KB L1 Instruction Cache
 512KB L2 Cache
 <8 mm² CPU Core+L2
 5.x mm² CPU Core
 (w/o bus unit <4 mm²)

ARM Cortex-A9 @28nm



28 nm bulk Process
 2-Decode Out-of-Order
 32KB L1 Data Cache
 32KB L1 Instruction Cache
 Up to 2MB L2 Cache
 1.15mm² (1 CPU Core)
 (Nvidia)

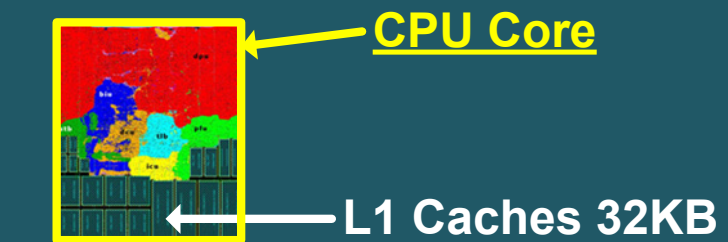
Intel Silvermont 2 CPU Core @22nm



22 nm LP bulk FinFET Process (Intel)
 2 CPU Cores
 2-Decode Out-of-Order
 24KB L1 Data Cache
 32KB L1 Instruction Cache
 1MB Shared L2 Cache
 8.x mm² CPU Core+L2
 2.x mm² CPU Core(w/o bus)

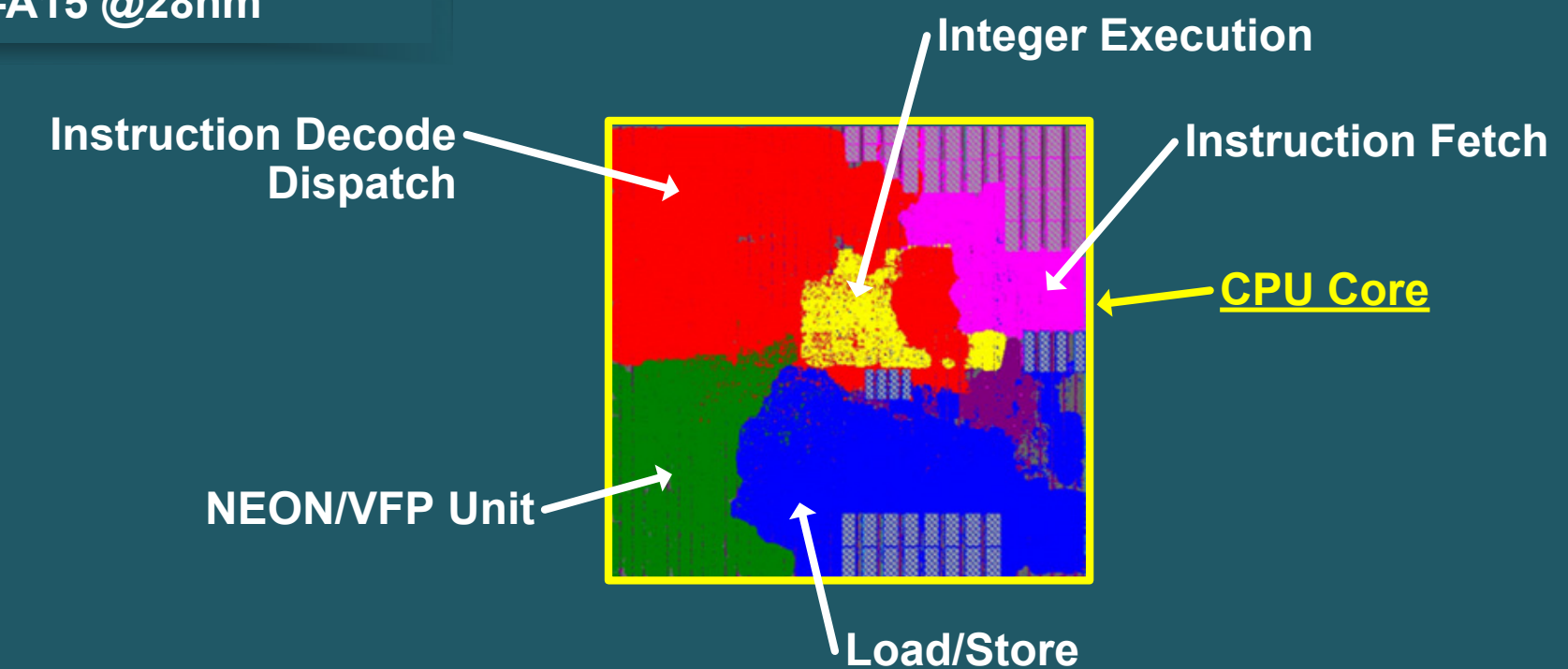
40.00倍

ARM Cortex-A7 @28nm



28 nm Process
 Limited 2-Decode In-Order
 32KB L1 Caches
 0.45mm² (1 CPU Core)

ARM Cortex-A15 @28nm



28 nm Process
 3-Decode Out-of-Order
 32KB L1 Data Cache
 32KB L1 Instruction Cache
 Up to 4MB L2 Cache
 2.7mm² (1 CPU Core)
 (Nvidia)