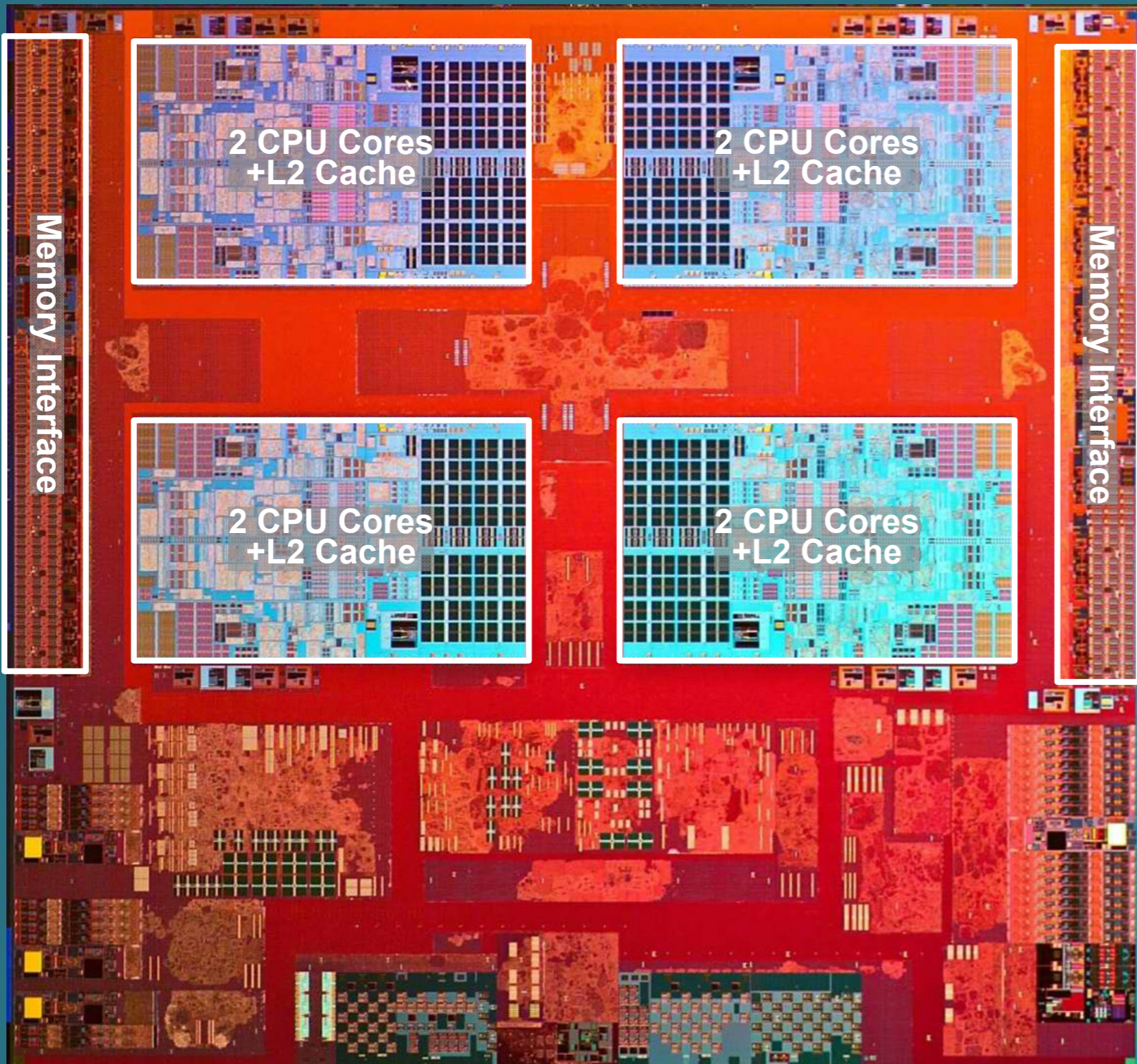


# Avoton Die



2 CPU Cores  
+L2 Cache

2 CPU Cores  
+L2 Cache

2 CPU Cores  
+L2 Cache

2 CPU Cores  
+L2 Cache

Memory Interface

Memory Interface

Silvermont  
2 CPU Cores  
+L2 Cache

**22 nm LP FinFET bulk Process (Intel)**  
**8 Silvermont Cores**  
**4 MB L2 Cache**  
**128-bit DDR3 IF**  
**PCIe x16**  
**>100 mm<sup>2</sup>**