AMD Trinity and Llano Die

CPU Modules
- 2x 2 MB L2 Cache
- PCI Express Gen 2 24 Lanes
- 32 nm SOI Process (GLOBALFOUNDRIES)
- 1.303 billion transistors
- 246 mm²

GPU Core
- (6 SIMDs, 384 Stream Processors)

Die Layouts

Trinity Die Layout

Llano Die Layout

Note: The diagrams show the architectural layout of the CPUs and GPU, including various components such as caches, memory controllers, and I/O interfaces.