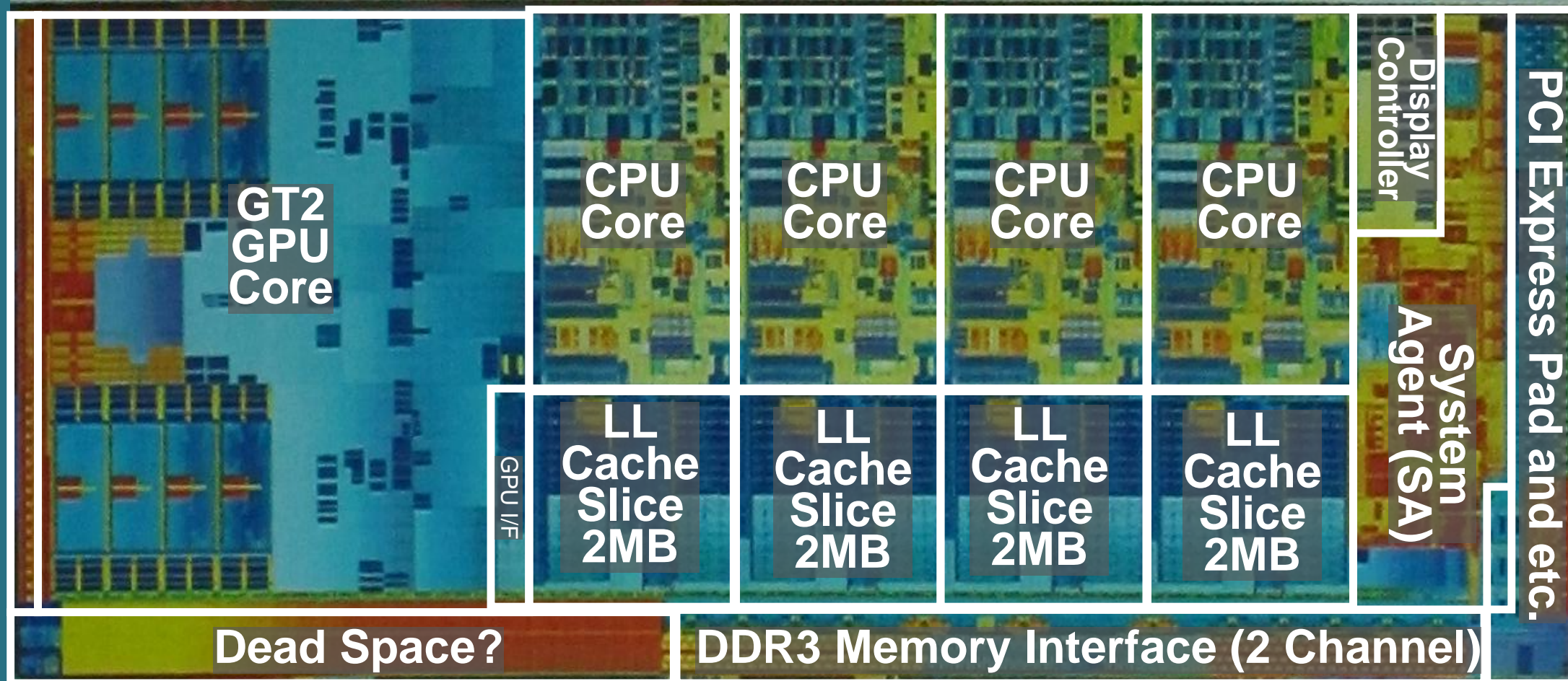


Ivy Bridge and Sandy Bridge Die Layout (Estimated)

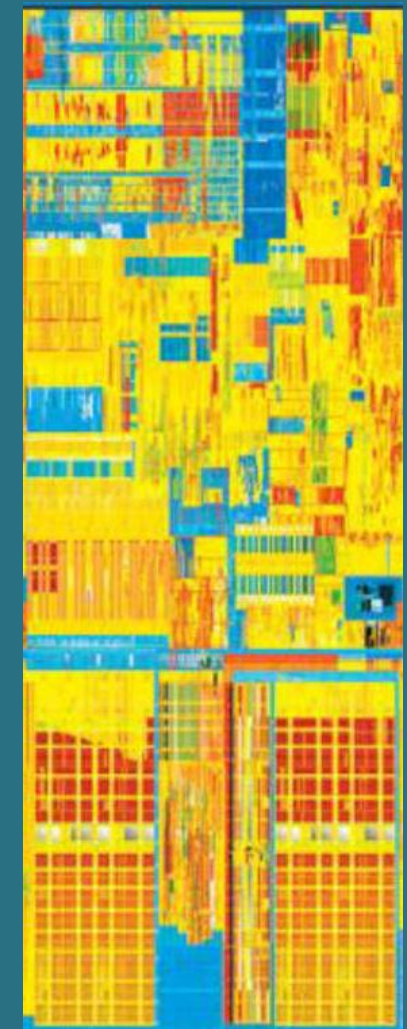
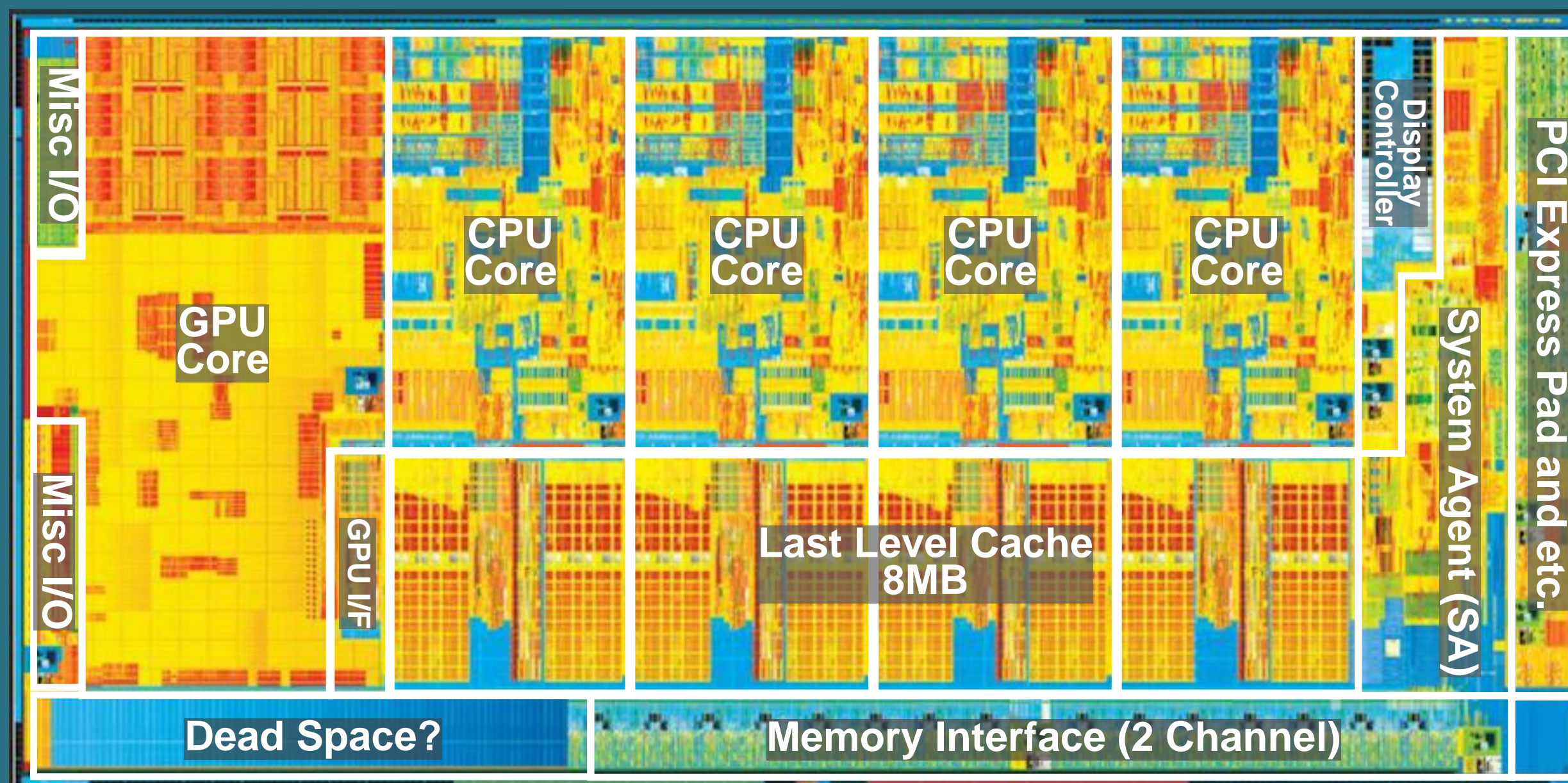
Ivy Bridge 22nm



4 CPU cores
GPU core
8MB LL Cache

22 nm Process
1.48B transistors
160 mm²
TDP

Sandy Bridge 32nm



4 CPU cores
GPU core (12 EUs)
8MB LL Cache
PCI Express Gen2 20 Lanes

32 nm Process
1.16B transistors
216 mm²
TDP 95/65/45W(Desktop)