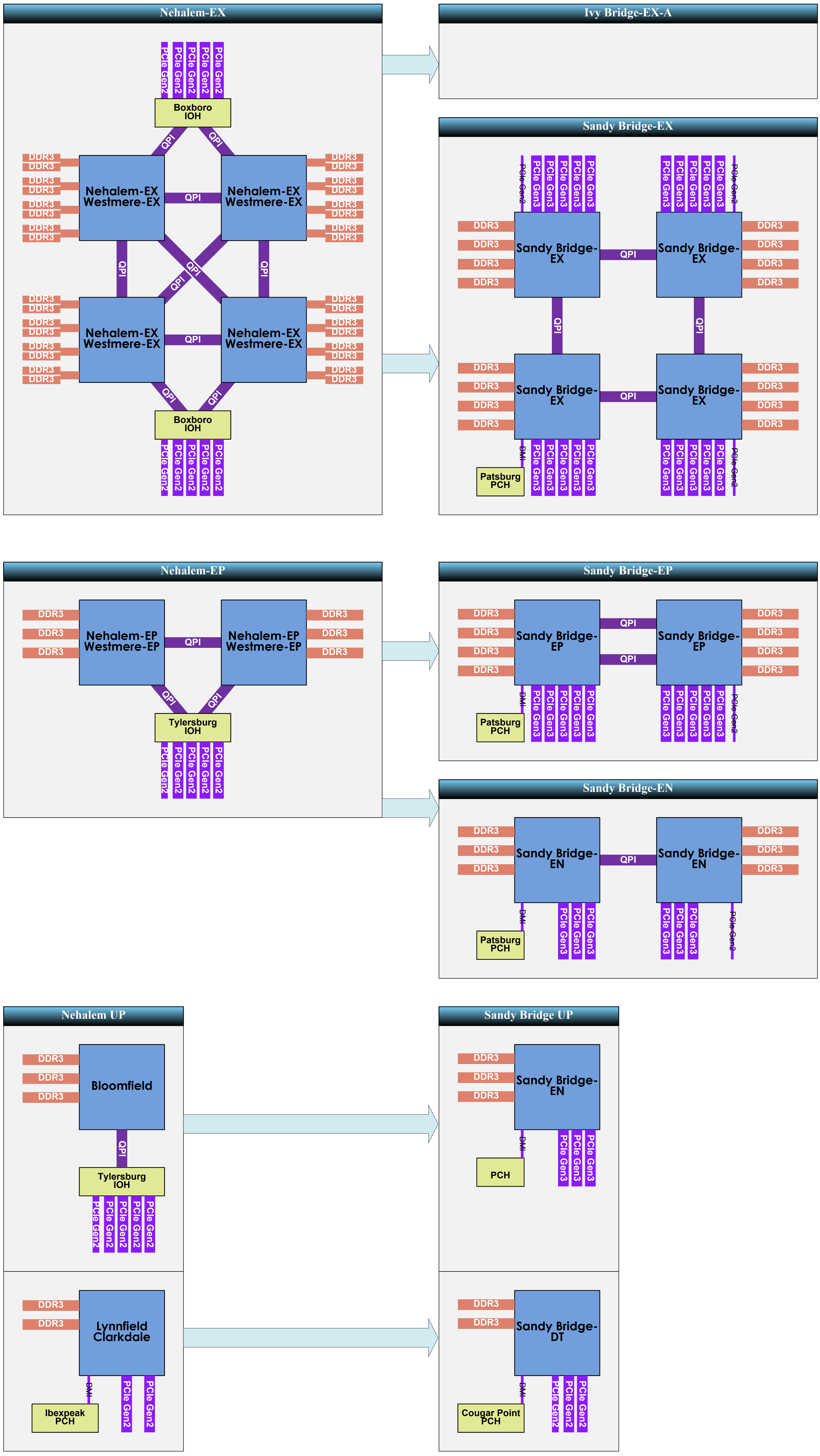Sandy/Ivy Bridge Multi-Processor Configuration