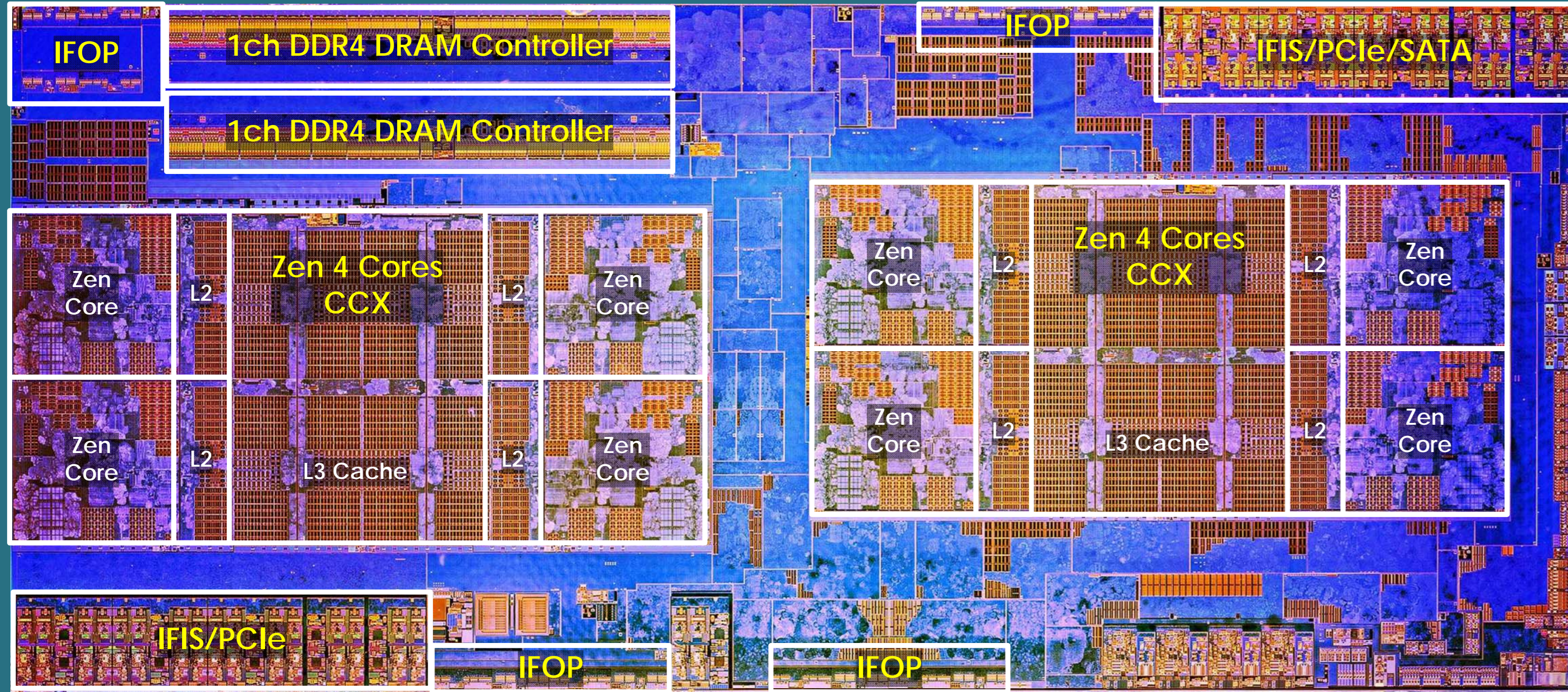


Zeppelin Die Layout

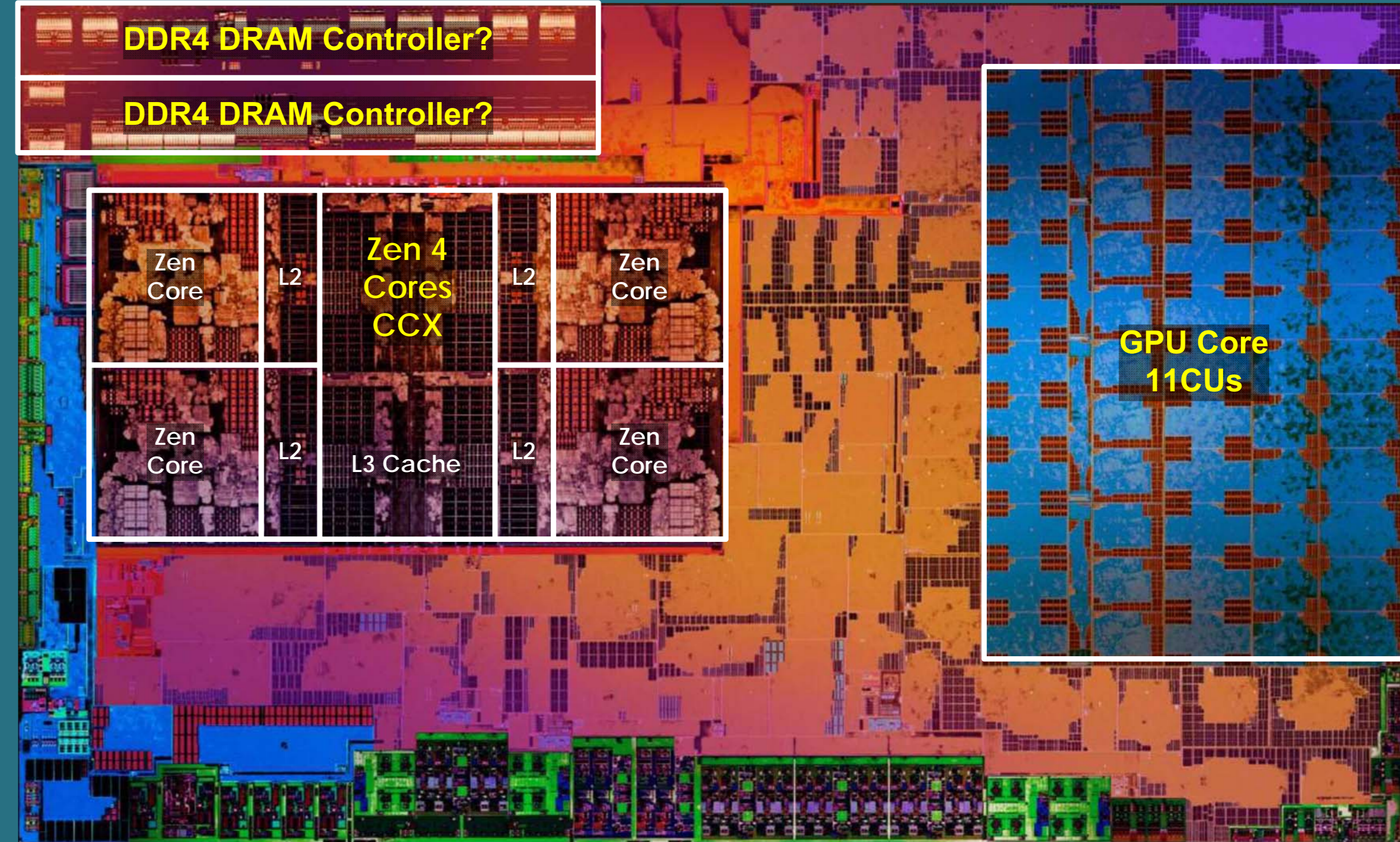


Zeppelin Die
 8 ZEN CPU cores(16 Threads)
 4MB L2 Cache(512KBx8)
 16MB L3 Cache

14 nm FinFET Process
 (GLOBALFOUNDRIES 14LPP)
 4.8B transistors
 212.97 mm²

8 CPU Cores

Raven Ridge Die Layout



4 ZEN CPU cores(8 Threads)
 2MB L2 Cache(512KBx8)
 4MB L3 Cache

14 nm FinFET Process
 (GLOBALFOUNDRIES 14LPP)
 4.94B transistors
 209.78 mm²

4 CPU Cores and 11-CU GPU