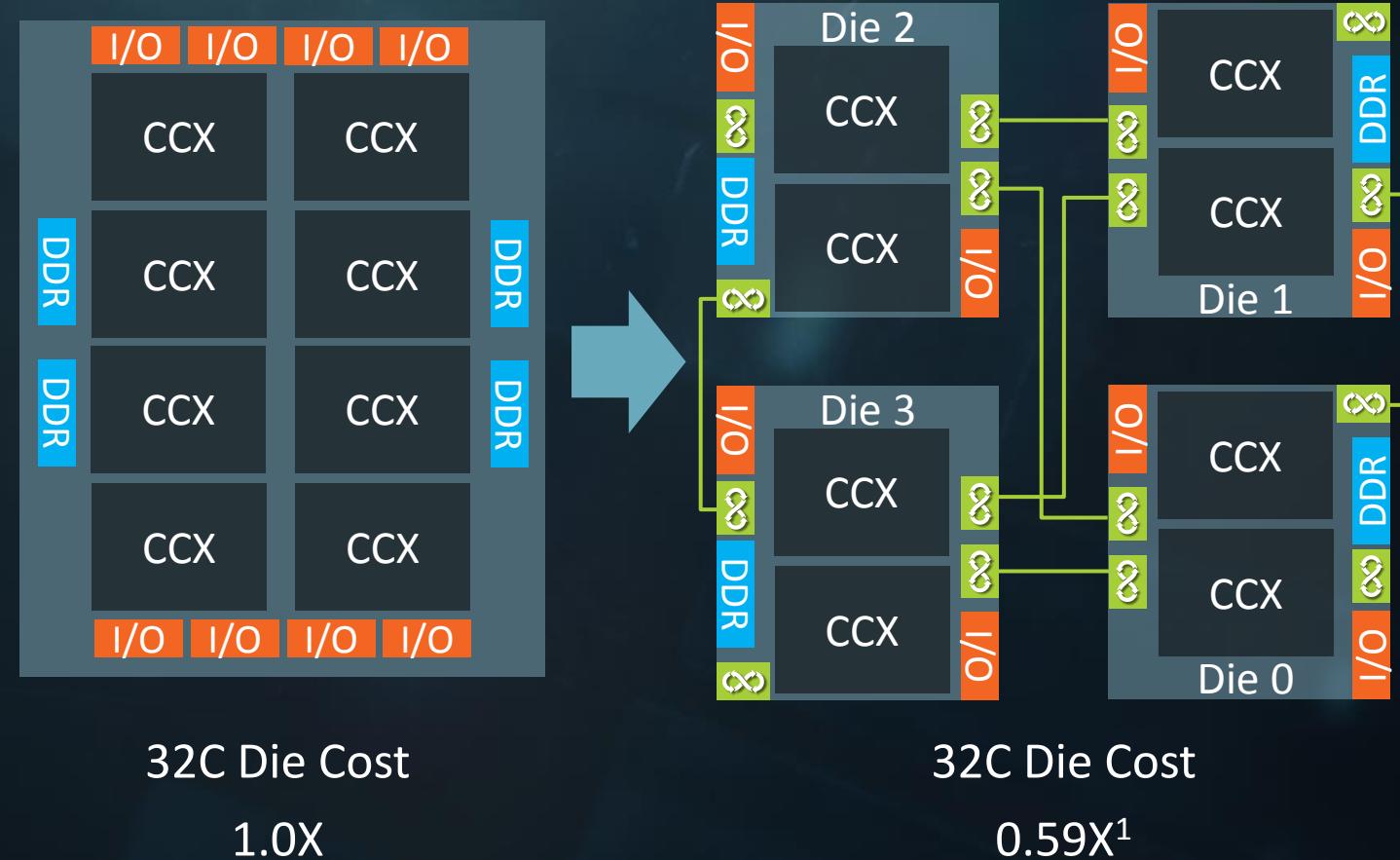


MCM VS. MONOLITHIC

- MCM approach has many advantages
 - Higher yield, enables increased feature-set
 - Multi-product leverage
- EPYC™ Processors
 - $4 \times 213\text{mm}^2 \text{ die/package} = 852\text{mm}^2 \text{ Si/package}^*$
- Hypothetical EPYC Monolithic
 - $\sim 777\text{mm}^2$ *
 - Remove die-to-die Infinity Fabric PHYs and logic (4/die), duplicated logic, etc.
 - $852\text{mm}^2 / 777\text{mm}^2 = \sim 10\%$ MCM area overhead
- Inverse-exponential reduction in yield with die size
 - Multiple small dies has inherent yield advantage



MCM Delivers Higher Yields, Increases Peak Compute, Maximizes Platform Value

* Die sizes as used for Gross-Die-Per-Wafer