big.LITTLE with DynamIQ Cluster

- Little CPU Cluster
- Big CPU Cluster

- Shared L2 Cache
- CCI (Cache Coherent Interconnect)
- Arbitration and Snoop Control Unit

- Shared L3 Cache

- big CPU
  - L1
  - L2

- Medium CPU
  - L1
  - L2

- Medium CPU
  - L1
  - L2

- big CPU
  - L1
  - L2

- Little CPU
  - L1
  - L2

- Little CPU
  - L1
  - L2

- Little CPU
  - L1
  - L2

- Little CPU
  - L1
  - L2

- Little CPU
  - L1
  - L2