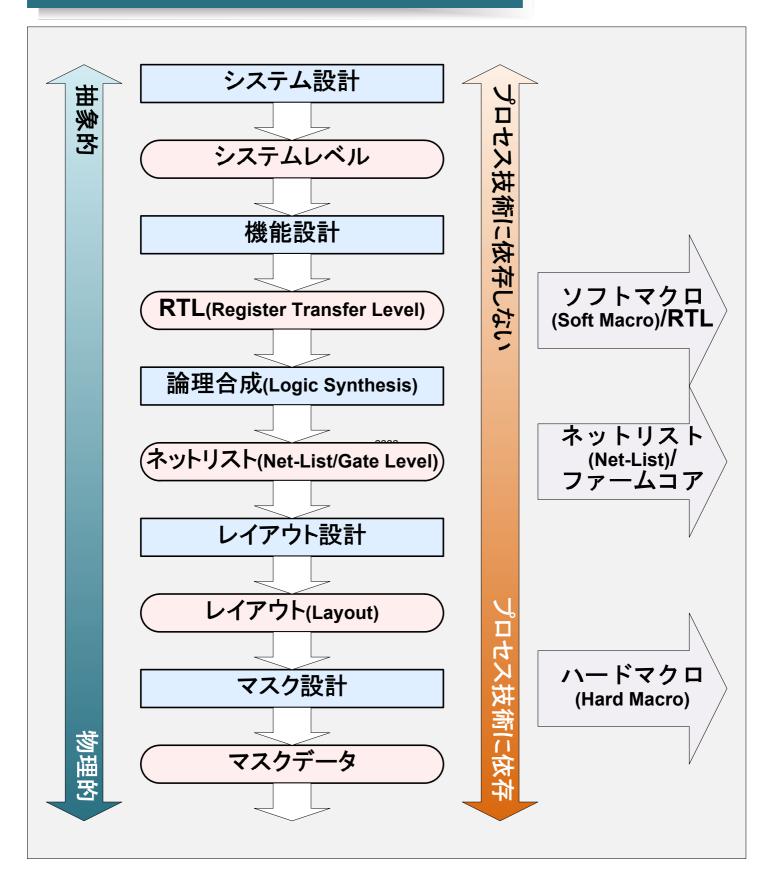
## 論理合成する場合のLSI設計のフロー



Copyright (c) 2014 Hiroshige Goto All rights reserved.