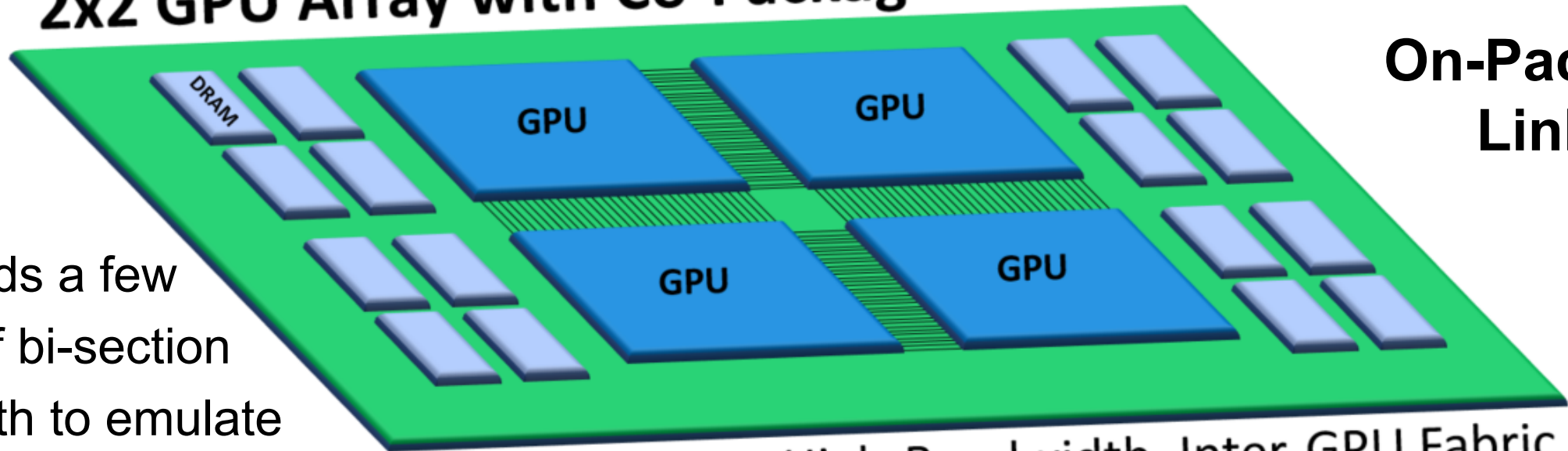


# Systems of the Future

- Witnessing increasing levels connectivity ... as systems require more bandwidth
- Eventually... reticle, yield, and performance limits will force co-packaged solutions

## 2x2 GPU Array with Co-Packaged DRAM



**On-Package  
Links**

Needs a few  
TB/s of bi-section  
bandwidth to emulate  
a single giant GPU

Energy-Efficient, High-Bandwidth, Inter-GPU Fabric  
(On-Package GRS Links)