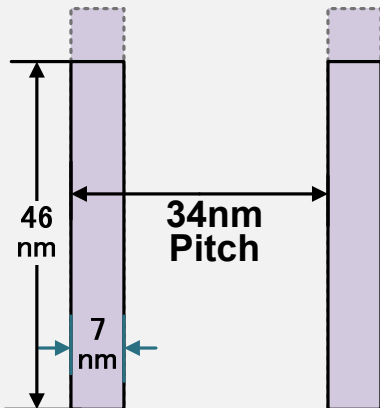
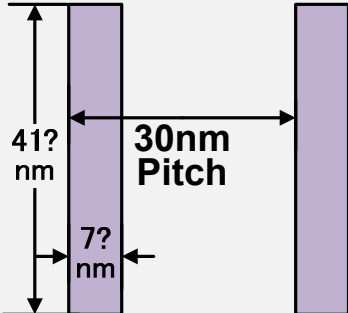


## Layer Pitches (-M5)

	Intel 10nm CPU?	TSMC 7nm SRAM(IEDM 2016)	GLOBALFOUNDRIES 7nm HPC
M5	52nm Pitch	76nm Pitch	80nm Pitch
M4	44nm Pitch	40nm Pitch	80nm Pitch
M3	44nm Pitch	40nm Pitch	40nm Pitch
M2	44nm Pitch	40nm Pitch	40nm Pitch
M1	36nm Pitch	40nm Pitch	56nm Pitch
M0	40nm Pitch	40nm Pitch	40nm Pitch
Contacted Gate	54nm Pitch	54nm Pitch	56nm Pitch
Fin	 <p>46 nm 7 nm 34 nm Pitch</p>		 <p>41? nm 7? nm 30 nm Pitch</p>