

Integrated Power Gate

- Clock Gate
 - Exists in all modern Intel processors
 - Shuts off switching power from idle logic but leakage power remains
- Power Gate: **New**
 - Shuts off both switching power and leakage power
 - Enables idle cores to go to ~ 0 power (C6), independent of state of other cores on die
 - Completely transparent to platform and software, no incremental platform cost

