

# SilverthorneのCステイト制御

	C0 High Frequency Mode	C0 Low Frequency Mode	C1/C2	C3(Dep Sleep)	C4(Deeper Sleep)	C6(Deep Power Down)
Core Voltage						
Core Clock						
PLL						
L1 Cache						
L2 Cache						
Wakeup time						
Power						
Trade Off			<p>High Idle Power ← Low Idle Power</p> <p>Quick wake up ← Long Latency</p>			