

Intel Mobile CPU (2007-2008)

Brand	Core 2 Duo		Celeron	Core 2 Duo			Core 2 Extreme	
Processor #	T8xxx	T9xxx					X9xxx	
Voltage	Standard V		Standard V					
Codename	Penryn 3M	Penryn 6M	Penryn-L	Penryn 3M	Penryn 6M		Penryn 6M	Penryn QC
Platform	Santa Rosa		Montevina			Santa Rosa	Montevina	
Micro-architecture	Core MA		Core MA			Core MA		
# of Core	Dual Core		Single Core	Dual Core		Dual Core		Quad Core
Hyper-Threading	N/A		N/A			N/A		
Intel 64	Intel 64		Intel 64			Intel 64		
VT	VT		VT			VT		
EIST(SpeedStep)	EIST(SpeedStep)		EIST(SpeedStep)			EIST(SpeedStep)		
IDA	IDA?		N/A	IDA		N/A		
Cache	3MB L2	6MB L2	2MB L2?	3MB L2	6MB L2		6MB L2	2x6MB
FSB	FSB 800			FSB1066		FSB 800	FSB1066	
Memory interface	N/A		N/A			N/A		
Package	PGA/BGA		PGA	PGA/BGA 22mm	PGA	PGA/BGA	PGA	
Socket	Socket P		Socket P			Socket P		
Process Technology	45nm		45nm			45nm		
# of Die	1		1			1	2	
TDP	35W		35W	25W	35W	45W		
Launch	Q1'08		Q3'08	Q2'08		Q1'08	Q2'08	Q3'08

Brand	Celeron M	Celeron	Core 2 Duo				Core 2 Extreme
Processor #	520/530	540-	T5500	T5600	T7200/7400/7600	T7100/7250	T7x00
Voltage	Standard V		Standard V				
Codename	Merom-L	Merom-L	Merom (Napa Refresh)		Merom (Santa Rosa)		Merom
Platform	Napa	Santa Rosa	Napa		Santa Rosa		Santa Rosa
Micro-architecture	Core MA		Core MA				Core MA
# of Core	Single Core		Dual Core				Dual Core
Hyper-Threading	N/A		N/A				N/A
Intel 64	Intel 64		Intel 64				Intel 64
VT	N/A		N/A	VT		VT	
EIST(SpeedStep)	N/A		EIST(SpeedStep)				EIST(SpeedStep)
IDA	N/A		N/A		IDA		N/A
Cache	1MB L2		2MB L2	4MB L2	2MB L2	4MB L2	4MB L2
FSB	FSB533		FSB 667		FSB 667	FSB 800	FSB 800
Memory interface	N/A		N/A				N/A
Package	PGA		PGA/BGA		PGA/BGA		PGA
Socket	Socket M	Socket P	Socket M		Socket P		Socket P
Process Technology	65nm		65nm				
# of Die	1		1				1
TDP	31W		34W		35W		44W
Launch	Q1'07	Q3'07	Q3'06		Q2'07		Q3'07

Brand	Celeron M	Core 2 Solo	Core 2 Duo				Celeron	Core 2 Duo	
Processor #	523	U2100/2200	U7500/7600	U7500/7600/7700	L7200/7400	L7300/7500/7700			
Voltage	ULV	ULV		LV		ULV	ULV	LV	
Codename	Merom-L	Merom-L	Merom	Merom	Merom	Merom	Penryn-L	Penryn 3M	
Platform	Napa	Napa		Santa Rosa	Napa	Santa Rosa	Montevina		
Micro-architecture	Core MA		Core MA				Core MA		
# of Core	Single Core	Single Core	Dual Core			Single Core	Dual Core		
Hyper-Threading	N/A		N/A				N/A		
Intel 64	Intel 64		Intel 64				Intel 64		
VT	N/A		VT				VT		
EIST(SpeedStep)	N/A		EIST(SpeedStep)				EIST(SpeedStep)		
IDA	N/A		N/A		IDA	N/A	IDA		
Cache	1MB L2	1MB L2	2MB L2		4MB L2		2MB L2	3MB L2	
FSB	FSB533	FSB533		FSB 667	FSB 800		FSB1066		
Memory interface	N/A		N/A				N/A		
Package	BGA	BGA		BGA		BGA 22mm	BGA 22mm		
Socket	Socket M	Socket M		Socket P	Socket M	Socket P	Socket P		
Process Technology	65nm		65nm				45nm		
# of Die	1		1				1		
TDP	5.5W	5.5W	10W		17W		5.5W	10W	
Launch	Q3'07	Q3'07	Q2'07	Q3'07	Q1'07	Q2'07	Q3'08	Q3'08	

Brand	Celeron M		Core Solo	Core Duo	
Processor #	4x3	4x0	U1xxx	U2xxx	L2xxx
Voltage	ULV	Standard V	ULV		LV
Codename	Yonah SC		Yonah SC	Yonah DC	
Micro-architecture	Yonah		Yonah		
# of Core	Single Core		Single Core	Dual Core	
Hyper-Threading	N/A		N/A		
Intel 64	N/A		N/A		
VT	N/A		VT	VT	
EIST(SpeedStep)	N/A		EIST(SpeedStep)		
Cache	1MB L2		2MB L2		
FSB	FSB533		FSB533		FSB 667
Package	PGA		BGA		
Process Technology	65nm		65nm		
TDP	5.5W	27W	5.5W	9W	15W
Launch	Q2'06	Q2'06	Q2'06	Q3'06	Q1'06