Intel's MP Server CPU transition

Dual-Core Xeon 71xx(Tulsa)	Quad-Core Xeon 73xx(Tigerton)	Dunnington	Nehalem-EX(Beckton)
Native 2-core CPU CPU Core 1MB L2 16MB Shared L2 Cache FSB	CPU CORE AMB Shared L2 Cache FSB FSB FSB FSB	CPU Core CPU Core Core CPU CORE CORE CPU CPU CPU CORE CPU	Native 8-core CPU CPU CPU CPU CPU CPU CPU CPU Core Core Shared Cache? Shared Cache? Shared Cache? 24MB Shared Last Level Cache FBD2 Memory Controller QPI Link Controller
NetBurst Microarchitecture	Core Microarchitecture	Core Microarchitecture	Nehalem Microarchitecture
2 cores	4 cores	4/6 cores	up to 8 cores
4 threads	4 threads	4/6 threads	up to 8 threads
Single die	2 dies	Single die	Single die
16MB L3	2x4MB L2	12/16 MB L3	24MB L?
Single FSB (800Mtps)	Shared FSB (1066Mtps)	Single FSB (1066Mtps)	4 QPI (6.4Gtps)
65nm	65nm	45nm	45nm
95/150W	50/80/130W	60/90/130W	90/130W
Q3 '06	Q3 '07	Q4 '08	2H '09