

Dynamic Range of Design Choices



Lever	Range	Factor
Pipeline Depth	5-25 stages	5x
Cache	16K-2M bytes	125x
ILP	1-10 units	10x
Voltage	0.7-1.4 volts	2x
IO Pins	100-500	5x
Frequency	300-3000 MHz	10x
Leakage (Idoff)	1-1000 nA/um	1000x
Die Size	30-300 mm ²	10x
Dynamic Power	0.2-100 W	500x

All can be varied independent of instruction set.