

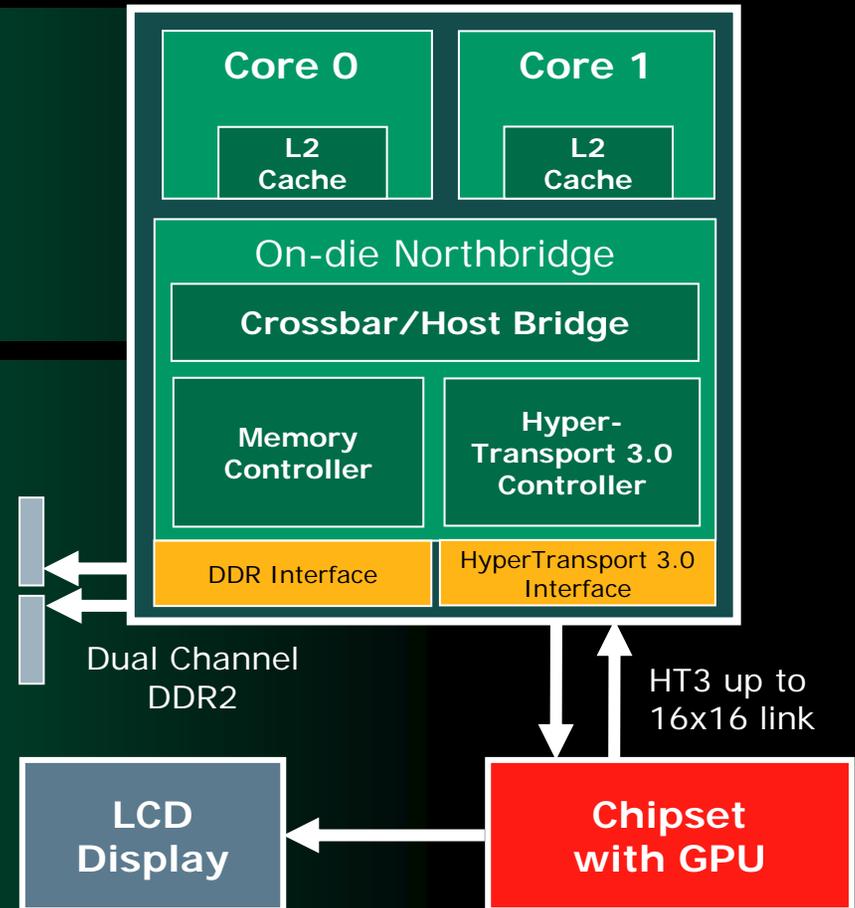
Mobile Optimized Memory Controller

New integrated memory controller features:

- DRAM pre-fetcher
- Improvements in DRAM efficiency

Extended battery life with new power saving features

- Operates on separate power plane, and at a lower voltage than the cores
- Enables C4 Deeper Sleep on systems with UMA graphics without the need for local frame buffer



Cost and power efficient solution for UMA

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