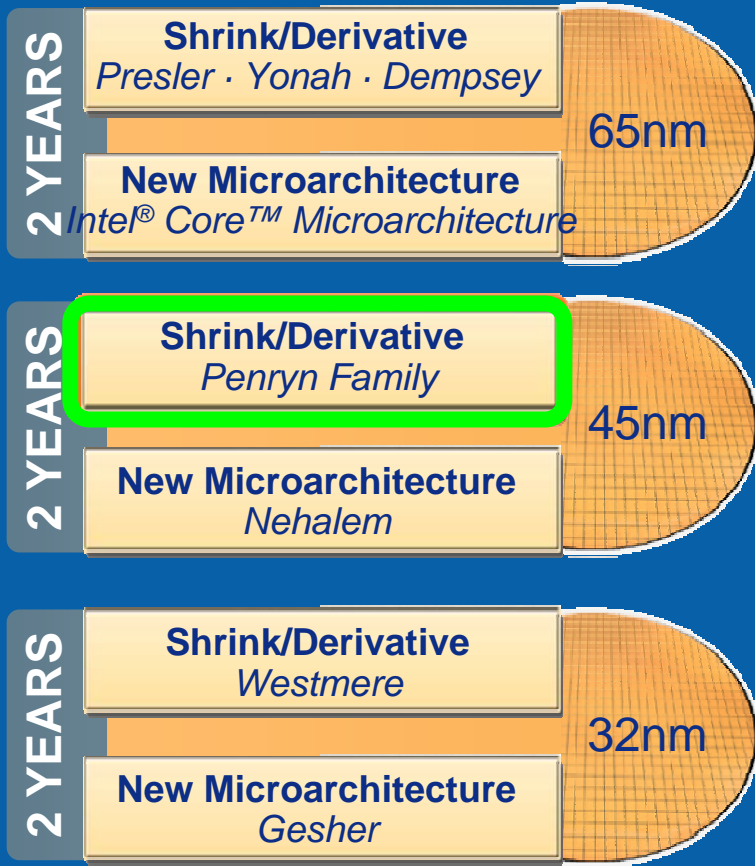


Penryn Family Design Goals



Increase performance per given clock cycle

Increase processor frequencies

Extend energy efficiency

Deliver lead product for 45nm High k + metal gate process technology

Deliver optimized processors across each product segment and power envelope