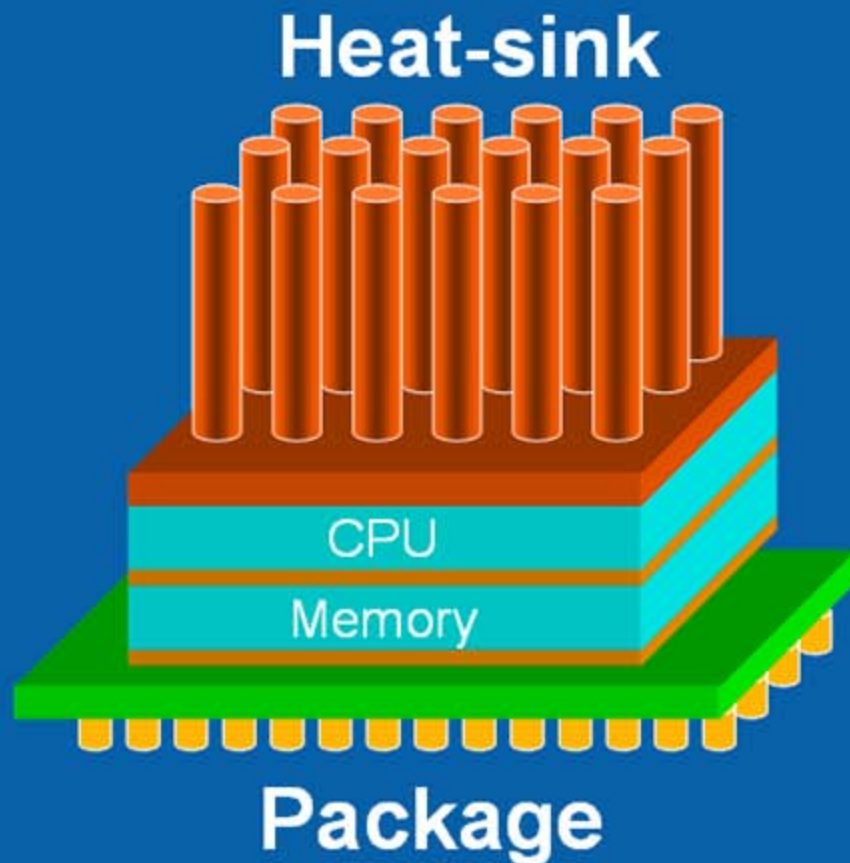


Memory at the Bottom



Power and IO signals go through Memory to CPU

Thin Memory die

Through Memory vias