

# 予定されていたPentium 4の機能進化

	Willamette	Northwood	Northwood(HT)	Prescott	Tejas	Cedarmill
MicroArchitecture					Enhanced Hyper-Threading	
					Tejas New Instructions(TNI)	
				Clackamas	Clackamas	
				LaGrande(Vanderpool)	LaGrande/Vanderpool	
				SSE3(Prescott New Instructions)		
				Improved Hyper-Threading		
		Hyper-Threading		Hyper-Threading		
		SSE2				
		Rapid Execution Engine				
		NetBurst Architecture				
Hardware Features	~ 2GHz	~ 2.8GHz	~ 3.4GHz?	~ 4.4GHz?	~ 5GHz?	~ 7GHz?
		12K uOPs Instruction Trace Cache			16K uOPs Trace Cache	
		8KB L1 Data Cache		16KB L1 Data Cache	24KB L1 Data Cache	
	256KB L2Cache	512KB L2Cache		1MB L2Cache	2MB L2Cache	
	FSB 400MHz	FSB 533MHz		FSB 800MHz		FSB 1066MHz
	PGA423 Package	mPGA478Package			LGA775 Package	
	217mm2 Die Size	146mm2 Die Size	131.4mm2 Die Size	112mm2 Die Size	130mm2? Die Size	
	0.18 μ m Process	0.13 μ m Process		90nm Process		65nm Process