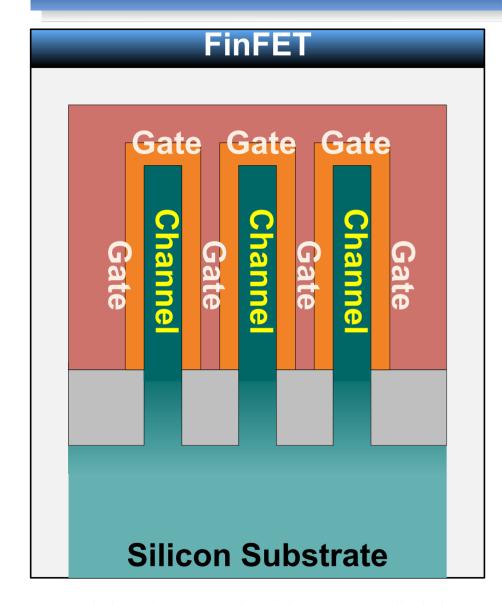
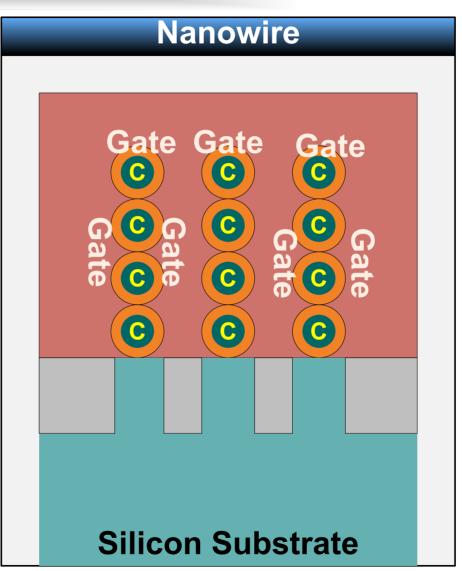
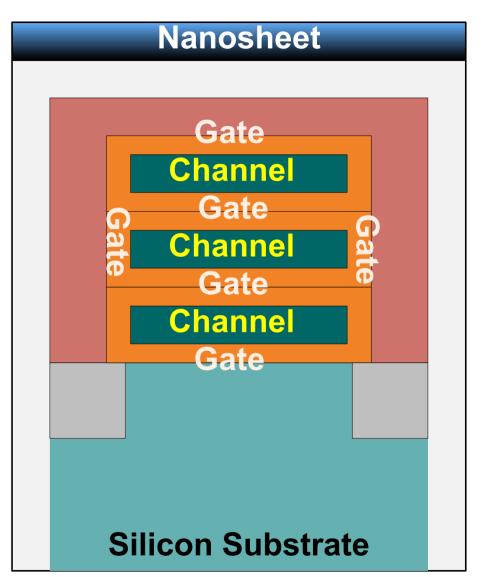
FinFET、Nanowire、Nanosheetのトランジスタ断面図







Copyright (c) 2017 Hiroshige Goto All rights reserved.