

# Deep Power Down Technology

## Available on Mobile Penryn Processor Family

	<u>C0</u> Active	<u>C1</u> Halt	<u>C2</u> Stop Clock	<u>C3</u> Deep Sleep	<u>C4</u> Deeper Sleep	Enhanced Deeper Sleep w/L5 I/O read	<u>C6</u> Deep Power Down
Core voltage*							
Core clock							
PLL							
L1 caches							
L2 cache							
Wakeup time*	active						
Idle power*							

New Power Management State  
Further Extends Battery Life

New for Penryn

