
































Deep Power Down Technology

Available on Mobile Penryn Processor Family

	C0 Active	C1 Halt	C2 Stop Clock	C3 Deep Sleep	C4 Deeper Sleep	Enhanced Deeper Sleep w/L5 I/O read	C6 Deep Power Down
Core voltage*							
Core clock		off	off	off	off	off	off
PLL				off	off	off	off
L1 caches				flushed	flushed	flushed	off
L2 cache					partial flush	off	off
Wakeup time*	active						
Idle power*							

New for Penryn

New Power Management State
Further Extends Battery Life



* Rough approximation