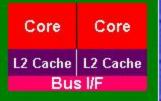
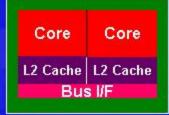
## Paxville and Tulsa Bus Interface

- MP chipset supports up to 4 dual-core processors
  - Shared driver on processor permits 2 sockets per bus
- Boot strap processor depends on configuration.
  - Will change with quantity and location of processors
  - May also change in the event of a field upgrade







Core Core L2 Cache L2 Cache Bus I/F

Intel E8500 and E8501 Chipsets